

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	129588	(semiconductor or integrated adj circuit) and (i/o\$1 or input\$4/output\$4 or input\$4 adj input\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/22 13:28
L2	39498	1 and driver\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/22 13:28
L3	1379	2 and delay\$4 near element\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/22 13:29
L4	1359	3 and control\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/22 13:29
L5	690	4 and propagat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/22 13:30
L6	428	5 and test\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/22 13:31
L7	78	6 and accelerat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/22 13:33